2 bit half adder using nand gates only

**Truth table:**

**a b x y**

**(s) (c)**

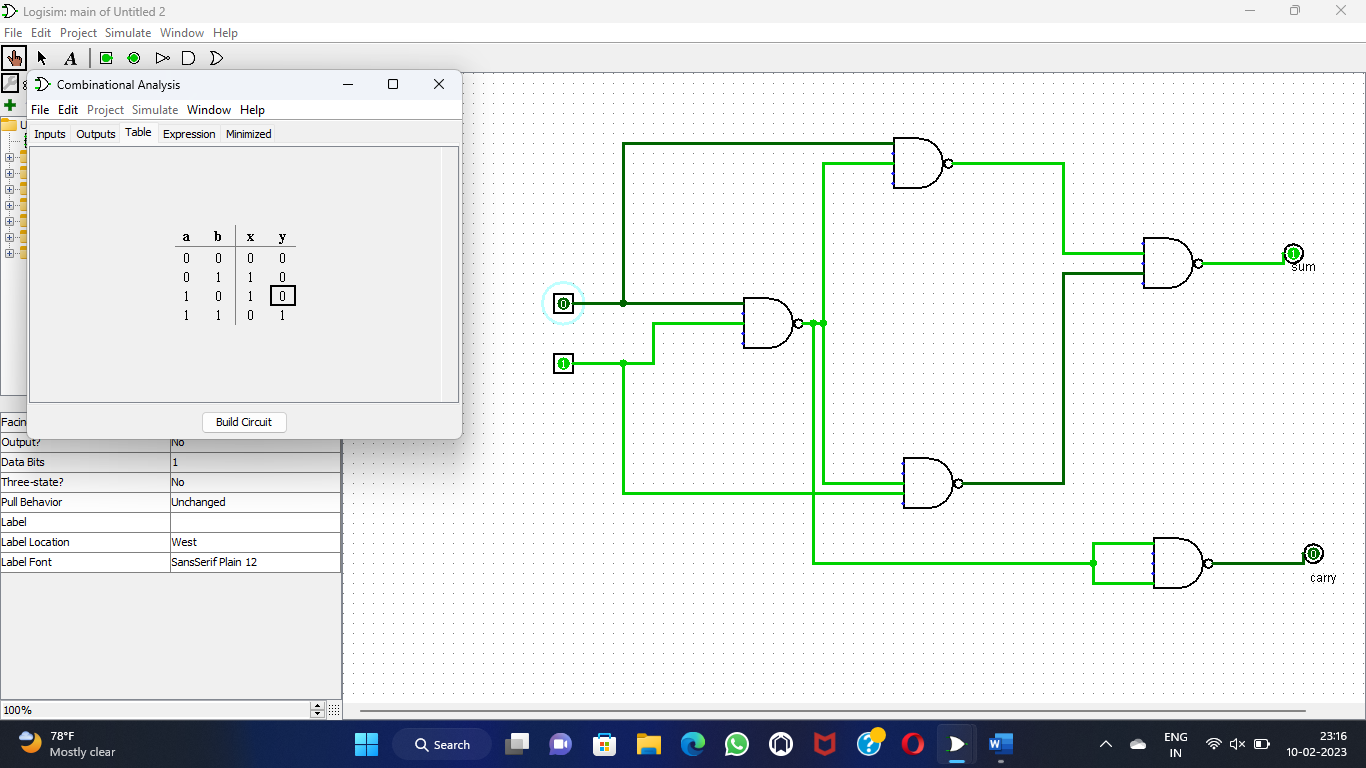
**0 0 0 0**

**0 1 1 0**

**1 0 1 0**

**1 1 0 1**

**Program output:**

****